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Resonant tunnelling features in a suspended silicon nanowire single-hole transistor

Jordi Llobet,¹ Emiljana Krali,² Chen Wang,² Jordi Arbiol,^{3,4} Mervyn E. Jones,² Francesc Pérez-Murano,^{1,a)} and Zahid A. K. Durrani^{2,a)}

¹*Institut de Microelectrònica de Barcelona (IMB-CNM CSIC), Campus UAB, E-08193 Bellaterra, Catalonia, Spain*

²*Department of Electrical and Electronic Engineering, Imperial College London, South Kensington, London SW7 2AZ, United Kingdom*

³*Institució Catalana de Recerca i Estudis Avançats (ICREA) and Institut Català de Nanociència i Nanotecnologia (ICN2), Campus UAB, 08193 Bellaterra, Catalonia, Spain*

⁴*CELLS-ALBA Synchrotron Light Facility, 08290 Cerdanyola, Catalonia, Spain*

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Suspended silicon nanowires have significant potential for a broad spectrum of device applications. A suspended *p*-type Si nanowire incorporating Si nanocrystal quantum dots has been used to form a single-hole transistor. Transistor fabrication uses a novel and rapid process, based on focused gallium ion beam exposure and anisotropic wet etching, generating <10 nm nanocrystals inside suspended Si nanowires. Electrical characteristics at 10 K show Coulomb diamonds with charging energy ~ 27 meV, associated with a single dominant nanocrystal. Resonant tunnelling features with energy spacing ~ 10 meV are observed, parallel to both diamond edges. These may be associated either with excited states or hole–acoustic phonon interactions, in the nanocrystal. In the latter case, the energy spacing corresponds well with reported Raman spectroscopy results and phonon spectra calculations. © 2015 AIP Publishing LLC. [<http://dx.doi.org/10.1063/1.4936757>]

Double-clamped, suspended nanowires (NW) and nanotube structures present outstanding properties for nanomechanical resonator applications.^{1,2} These structures have been identified as highly promising building-blocks, for sensors with unprecedented mass detection resolution, towards the single-molecule level,^{3,4} for highly efficient thermoelectric “clean” energy generation,^{5,6} and for quantum computation.^{7,8} These nanostructures are also of great interest in investigations of the fundamental limits of signal processing,⁹ and characterising electrical, mechanical and material properties at reduced dimensions.¹⁰

Modification of suspended NW structures by the incorporation of quantum dots (QDs) provides a means to measure excited electronic states, and single-electron–phonon interactions in zero-dimensions, confined within a suspended QD cavity⁷ isolated from phonon modes in the substrate. Furthermore, as these interactions are at low energies corresponding to acoustic phonon like modes, it becomes possible to probe the low-energy part of the phonon spectrum, providing a complementary technique to conventional methods, such as Raman spectroscopy.¹¹ A range of phenomena may be investigated, e.g., phonon confinement, energy and band gaps,^{7,12} phonon blockade of electrical conduction,¹³ interactions between nanomechanical and electron motion,^{14,15} and decoherence effects in quantum computation devices.⁷ While suspended QDs have been widely investigated in materials such as carbon nanotubes (CNTs),¹⁶ there are comparatively few investigations in Si.^{7,17–19} Previous work has concentrated on *n*-type Si material only, using lithographically patterned QDs in crystalline Si,^{7,17,18} “naturally” formed QDs in nanocrystalline Si¹⁷ and

QDs formed by disorder.¹⁹ However, suspended *p*-type single hole transistors have not been investigated previously and even for the case of non-suspended devices,^{20–22} there are relatively few investigations compared with *n*-type SETs.^{23,24} Single-hole transistors extend the single-electron and QD family of devices to include complementary *n*- and *p*-type devices, greatly enhancing flexibility in the design of few-electron, nanoelectronic systems.^{24,25} In addition, fundamental differences exist in the physical properties of Si single-hole transistors in comparison with single-electron transistors, associated with a different effective mass for holes, and the requirement for valence band potential barriers.²⁰ In Si, for a QD of radius *r*, the reduced hole effective mass implies increased excited state energy $E \sim (\pi^2 \hbar^2)/(2mr^2)$, improving the likelihood of observing resonant tunnelling effects through the excited states. Furthermore, in the case of Si nanocrystal single-hole transistors, information can be extracted on the nature of the nanocrystal surface, as either the presence of hole traps or a SiO₂ layer on the surface is essential to create valence band tunnel barriers.

This paper presents the electrical characteristics of a suspended Si NW single-hole transistor incorporating <10 nm QDs. Coulomb diamonds^{24,25} with energy ~ 27 meV and resonant tunnelling features with energy ~ 10 meV, are observed. Resonant tunnelling features form conductance lines parallel to both diamond edges, not previously observed in suspended Si QDs. These may be associated with either excited hole states or hole–acoustic phonon interactions, within a single dominant nanocrystal QD. For the excited state model, states both above and below the Coulomb gap are needed to create lines parallel to both diamond edges. For the hole–phonon interaction model, both phonon emission and absorption may occur to create lines parallel to both diamond edges. This provides an additional mechanism to

^{a)}Authors to whom correspondence should be addressed. Electronic addresses: francesc.perez@csic.es and z.durrani@imperial.ac.uk

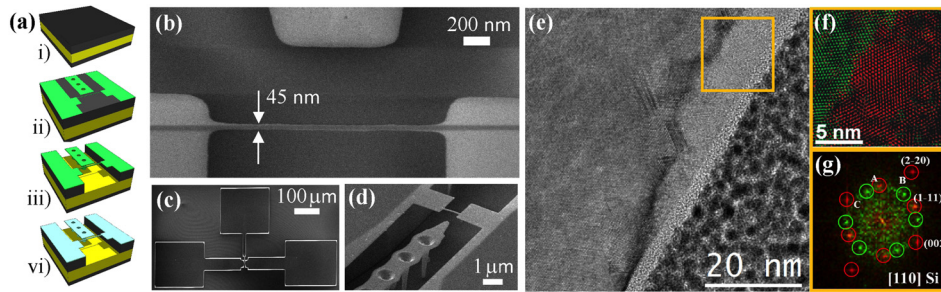


FIG. 1. (a) Schematic illustration of four steps during device fabrication process sequence, SEM micrographs showing (b) close-up of suspended Si NW and gate, (c) overall device, and (d) suspended gate showing pillar supports. (e) HRTEM image obtained at the area of one of the nanowires. After recrystallization, the NW presents stacking faults and twin boundaries as well as polycrystalline domains. The area selected in orange and zoomed on the right side shows a perfect [110] oriented grain (green), in contact with an off-axis silicon grain (partially rotated). (f) Corresponds to a combined reconstructed real space image obtained from the FFT spots (green or red) selected in the combined power spectra of (g). (g) The combined power spectra obtained on the orange selected area of the HRTEM image. Both the [110] oriented (green spots) and the off-axis domains (red spots) can be visualized.

create both sets of lines, increasing the likelihood of their occurrence. For the hole-phonon interaction case, a low-energy, acoustic phonon-like mode $\hbar\omega_p \sim 10$ meV can be associated with the dominant nanocrystal in the device. This corresponds well with reported Raman spectroscopy results¹¹ and phonon spectra calculations in Si nanocrystals.^{26,27}

The fabrication sequence and device structure is shown in Fig. 1(a), based on a novel focused ion beam (FIB) exposure and wet chemical etching process.²⁸ This provides a fast prototyping method for nanomechanical devices and for generating nanocrystals within suspended Si NWs. The starting substrates were taken from (110) SOI wafers, with top Si thickness $2 \pm 0.5 \mu\text{m}$ and nominal resistivity $9.5 \times 10^{-4} - 11 \times 10^{-4} \Omega\text{m}$ (*p*-type), and buried SiO₂ thickness $2 \mu\text{m} \pm 5\%$. The surface was patterned by FIB at a low exposure dose, creating amorphous Si areas with high gallium concentration. Exposed areas were resistant to wet Si etching and formed the device structural parts. Following exposure, suspended Si NWs were defined using wet anisotropic etching. With the appropriate device orientation, the nanowire remained completely suspended, while the clamping sites and pads were fully defined across the top Si layer. The suspended NW thickness was dictated by the range of implanted gallium, ~ 40 nm. Finally, annealing and boron doping of the device at $850^\circ\text{C} - 1000^\circ\text{C}$ recovered crystallinity and electrical conductivity, with final NW resistivity $\sim 4 \times 10^{-3} \Omega\text{m}$.

A SEM image of the suspended Si NW measured in this work is shown in Fig. 1(b). The NW was $2.15 \mu\text{m} \times 45 \text{ nm} \times 40 \text{ nm}$ in size, and suspended $1.8 \mu\text{m}$ above the SiO₂ substrate. A lower magnification SEM image of a similar device (Fig. 1(c)) also shows pads and contact lines. A gate electrode was defined in close proximity (700 nm) to the NW. In order to avoid its collapse, sustaining posts were defined by a combination of FIB implantation and milling (Fig. 1(d)).

The suspended NW structure was nanocrystalline due to amorphisation from the gallium implantation and subsequent recrystallisation. Figure 1(e) shows a high-resolution transmission electron microscopy (HRTEM) micrograph of the exposed and annealed area after recrystallisation. The NW volume presents several crystal defects, such as stacking faults and twin boundaries as reported previously,²⁸ with additionally, polycrystalline domains observed (see Fig. 1(e) right). Here, the bright, diagonal region corresponds to the recrystallised area, $\sim 60 \text{ nm}$ length and $\sim 10 \text{ nm}$ in maximum width. This region is composed of smaller nanocrystals

randomly oriented, with sizes ranging from ~ 5 to 10 nm . In the region marked by the square, an oriented crystal along the [110] zone axis faces another off-axis crystallite. The misoriented crystallite shows the characteristic HRTEM features of a Si bicrystalline nanoparticle rotated 60° from the [110] zone axis.²⁹ Bicrystalline Si nanodomains are common nanoparticles, and nanowires consist of nanostructures divided into two identical mirror halves joined by a {111} twin boundary.²⁹⁻³² In our previous work, Fig. 3(d) of Ref. 28, a brighter region showing several Moiré fringes is reported, corresponding to different crystals overlapping at random orientations. In this case, the size of the crystals also lies in the range between ~ 5 and 10 nm .

Electrical characterisation was performed using a CTI-Cryogenics closed cycle helium cryostat and an Agilent 4155B parameter analyser. ‘‘Coulomb diamond’’ single-hole tunnelling characteristics in a $40 \text{ nm} \times 45 \text{ nm} \times 2.15 \mu\text{m}$ suspended NW device at 10 K are shown in Figs. 2(a)–2(d). Similar behaviour is also observed in a second device. The drain-source current (I_{ds}) vs. drain-source (V_{ds}) and side-gate voltage (V_{gs}) are shown in Fig. 2(a), and the drain-source conductance (g_{ds}) vs. V_{ds} , V_{gs} in Figs. 2(b)–2(d). The I_{ds} – V_{ds} , V_{gs} characteristics (Fig. 2(a)) show non-linear I_{ds} – V_{ds} curves, modulated periodically as V_{gs} varies from 0 to 20 V . The I_{ds} – V_{ds} curves are broadly symmetric around $V_{ds} = 0 \text{ V}$. I_{ds} oscillates with V_{gs} with a large period $\Delta V_{gs1} \sim 7 \text{ V}$, and a finer, superimposed period $\Delta V_{gs1} \sim 1.1 \text{ V}$. The g_{ds} vs. V_{ds} , V_{gs} characteristics (Figs. 2(b)–2(d)) are shown using a three-dimensional plot (b) and using colour scale plots (c) and (d) to highlight trapezoidal Coulomb diamond regions and associated features. Three Coulomb diamonds are observed, where diamonds 1 and 2 (marked by white solid lines) are similar, and the diamond centred at $V_{gs} = 5 \text{ V}$ is reduced in width along V_{ds} . The fine oscillation in I_{ds} with V_{gs} (Fig. 2(a)) also occurs in g_{ds} , leading to an oscillation of the diamond edges in Figs. 2(c) and 2(d). Fine structure is also observed in Figs. 2(b) and 2(c), forming lines parallel to and outside the diamond edges, which will be discussed later.

The g_{ds} – V_{gs} characteristics at selected values of V_{ds} are shown in Figs. 3(a) and 3(b). Fine structure peaks are observed even within the diamond regions, e.g., at $V_{gs} \approx 14 \text{ V}$ (Fig. 3(b)). The g_{ds} – V_{ds} characteristics at $V_{gs} = 0 \text{ V}$ from 10 to 80 K are shown in Fig. 3(c). It is seen that both the central Coulomb blockade valley and fine structure persist to $\sim 40 \text{ K}$.

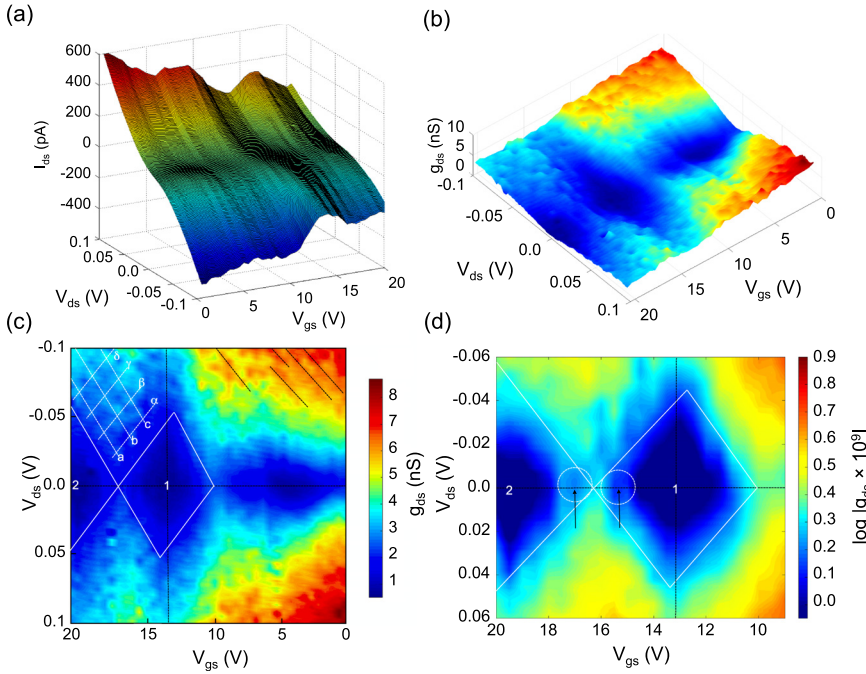


FIG. 2. Single-hole charging in the gated Si NW at 10 K. (a) I_{ds} vs. V_{ds} and V_{gs} characteristics, (b) g_{ds} vs. V_{ds} and V_{gs} characteristics, and (c) g_{ds} vs. V_{ds} and V_{gs} characteristics on a colour scale. Coulomb diamonds (labeled 1 and 2) are indicated by solid white lines and resonant tunnelling features are indicated using dashed white ($a-c$ and $\alpha-\delta$) and dashed black lines (in top right quadrant). These features can also be seen in (b). (d) Contour plot of $\log |g_{ds}|$ vs. V_{ds} and V_{gs} characteristics in the region of Coulomb diamonds 1 and 2 in (c).

The device characteristics can be associated with single-hole charging of a dominant island within the NW. Coulomb diamonds 1 and 2 are similar, implying a single, dominant island. The additional diamond centred at $V_{gs} = 5$ V is slightly wider along V_{gs} and narrower along V_{ds} , implying some change in this case for the island gate (C_g) and tunnel (C_1 and C_2) capacitances with V_{gs} .^{24,25} C_1 , C_2 , and C_g are now extracted²⁵ from diamonds 1 and 2. The diamond edges (solid white lines) are drawn such that they pass through the mid-point between the diamonds and are parallel to g_{ds} contours. The diamond 1 width is $\Delta V_{gs1} = 6.9$ V, implying $C_g = e/\Delta V_{gs1} = 0.023$ aF. The total island capacitance $C_t = C_1 + C_2 + C_g$ can be found using the V_{ds} value at the top or bottom corner, $V_{ds1} = e/C_t = 0.053$ V, giving $C_t = 3$ aF. The V_{ds} value of the intersection between the diamond edge and the vertical line through the diamond centre, $V_{dsi} = e/(2C_2 + C_g) = 0.043$ V, gives $C_2 = 1.85$ aF. Hence, using $C_t = 3$ aF, we find $C_1 = 1.13$ aF. Finally, island single hole charging energy is $E_c = e^2/2C_t = 27$ meV.

The origin of the QD creating the characteristics of Fig. 2 is now discussed. Holes from nanocrystals can be trapped at grain boundaries (GBs) in the nanocrystalline Si NW device channel, creating potential barriers.^{33,34} Many nanocrystals with a distribution of barrier heights exist along the NW (Fig. 3(d-i)), e.g., nanocrystal “A” has higher barriers than “B.” Conduction occurs via percolation through nanocrystals with the lowest GB barriers.³⁵ As the gate bias is increased, the valence band edge energy decreases, with accompanying depletion of hole concentration within the nanocrystals and an increase in the GB barrier seen by the holes (Fig. 3(d-ii)).³⁵ This picture is supported by the trend to reduced current with increasing V_{gs} (Fig. 2(a)). The electrical characteristics of Fig. 2 occur at the point where single-hole charging has only just begun, and only one nanocrystal (“A” in Fig. 3(d-ii)) is sufficiently isolated. At higher V_{gs} values, additional QDs would become isolated and a multiple tunnel junction (MTJ) would form, leading to more complex Coulomb diamond patterns.³⁶

The extracted capacitances may be used to estimate QD size and hence correlated with the nanocrystalline structure.

The gate capacitance for the entire NW, calculated using a finite-element capacitance calculation, $C_{NW} = 10$ aF, implies an NW capacitance per unit length $c_{NW} \sim C_{NW}/l_{NW} \sim 4.7$ aF/ μ m. As C_g for the charging island is only 0.023 aF, this implies a very small island length $l_i \sim 5$ nm, similar to the nanocrystal grain size.²⁸ Furthermore, assuming a spherical island of diameter d_i with self-capacitance $C_i = C_t = 2\pi\epsilon_0\epsilon_r d_i = 3$ aF gives $d_i \sim 4.5$ nm $\sim l_i$. The charging island can therefore be associated with a Si nanocrystal within the NW.

Features for negative V_{ds} are indicated by dashed lines in Fig. 2(c). Here, lines lying between diamonds 1 and 2 (labelled $\alpha-\delta$ and $a-c$) form a subsidiary diamond-like pattern. Similar, although less clear, features can be observed for positive V_{ds} , e.g., in the range 0.02 V $< V_{ds} < 0.1$ V and 15 V $< V_{gs} < 20$ V. In principle, the fine structure may be expected to occur symmetrically in V_{ds} . However, as the structure is superimposed on a background variation in g_{ds} created by the Coulomb diamonds, the background and any asymmetry in this influences the clarity of the structure. A larger change in background g_{ds} for positive V_{ds} , in the region between Coulomb diamonds 1 and 2 (Figs. 2(b) and 2(c)), also seen in Figs. 3(a) and 3(b), reduces clarity of the fine structure.

Typically, fine structure of the form of Fig. 2(c) is caused by electron tunnelling through excited states in the underlying QD, or phonon assisted inelastic electron tunnelling through the QD.^{37–39} We note that the pattern formed by the lines $\alpha-\delta$ and $a-c$ very closely resembles phonon assisted tunnelling patterns in suspended CNT QDs.³⁸ Additional features are observed within the Coulomb diamonds. Near the central point between diamonds 1 and 2 (Fig. 2(d)), circular features (arrowed, and marked by dashed circles) are formed due to conductance lines cutting into the diamonds. Regions of higher conductance also extend past the nominal diamond edge (white line), e.g., along the top left edge of diamond 1.

The fine structure in Figs. 2 and 3 can be created by excited states, or phonon assisted inelastic electron tunnelling in the QD. Conductance lines parallel to Coulomb diamond edges³⁷ are associated mainly with additional resonant

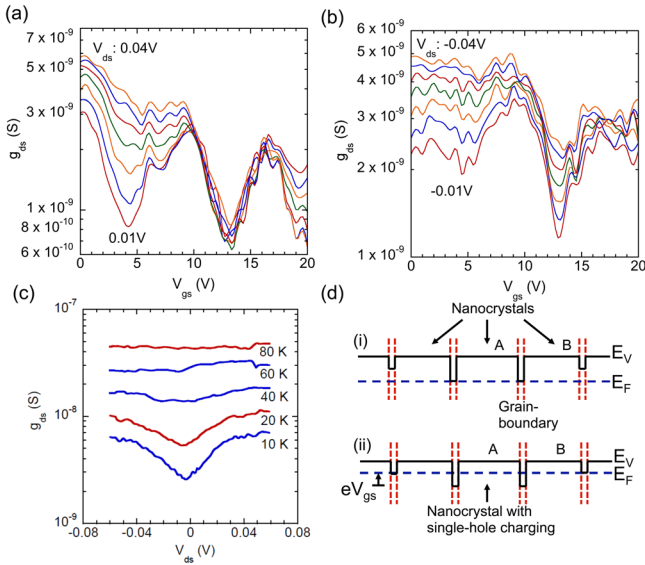


FIG. 3. Drain source conductance characteristics (a) g_{ds} vs. V_{gs} at selected values of $V_{ds} = 0.01$ V– -0.04 V, in increments of 0.005 V, (b) g_{ds} vs. V_{gs} at selected values of $V_{ds} = -0.01$ V to -0.04 V in increments of -0.005 V, (c) g_{ds} vs. V_{ds} characteristics from 10 to 80 K, at $V_{gs} = 0$ V. (d) Distribution of grain boundary potential barriers, shown schematically, (i) at zero gate bias and (ii) with applied gate bias $V_{gs} > 0$ V. As V_{gs} increases, single-hole charging occurs first in the nanocrystal (marked A) with the highest potential barriers.

tunnelling paths through excited states. While in many cases, asymmetrical QD tunnel barriers cause lines parallel to only one diamond edge,³⁷ less frequently, the presence of symmetrical barriers creates lines parallel to both edges⁴⁰ in a manner similar to our data. Alternatively, in suspended QDs in CNTs,³⁸ phonon assisted tunnelling can also create roughly equidistant conductance lines parallel to both diamond edges, very similar to our results. In CNTs, the energy scale of the lines enables differentiation between excited states and phonon-assisted tunnelling.³⁸ At elevated temperatures, phonon absorption causes conductance lines to extend into the Coulomb diamonds.³⁸ Finally, while fine structure has been reported in suspended QDs in n -type Si,^{17–19} lines parallel to the Coulomb diamonds have not been reported.

Energy band diagrams for the single-hole transistor Coulomb diamond are shown schematically in Fig. 4. A valence-band electron picture is shown for simplicity, with carrier transport from source to drain. Hole transport would occur concurrently along the same path, but from drain to source. E_N and E_{N+1} are the island chemical potentials for N and $N+1$ electrons, respectively. E_{FS} and E_{FD} are the source and drain Fermi energies. Dotted/solid lines lying above and below E_{N+1} and E_N show normally empty/filled states in an excited states picture.^{37,39} In the phonon assisted tunnelling picture,³⁷ these correspond to a ladder of phonon absorption (dotted lines) and emission (solid lines) levels, $E_{pn} = n\hbar\omega_p$ where ω_p is the fundamental phonon mode. Other absorption/emission levels can also exist, lying within E_c (not shown for clarity). At positions (a) and (c), Coulomb blockade is overcome, with $N+1$ and N electrons on the island, respectively. Position (b) corresponds to Coulomb blockade imposed fully. Position (e) shows the condition along the top left diamond edge, such that E_{N+1} remains aligned with E_{FS} . Along the top right edge, E_N remains aligned with E_{FD}

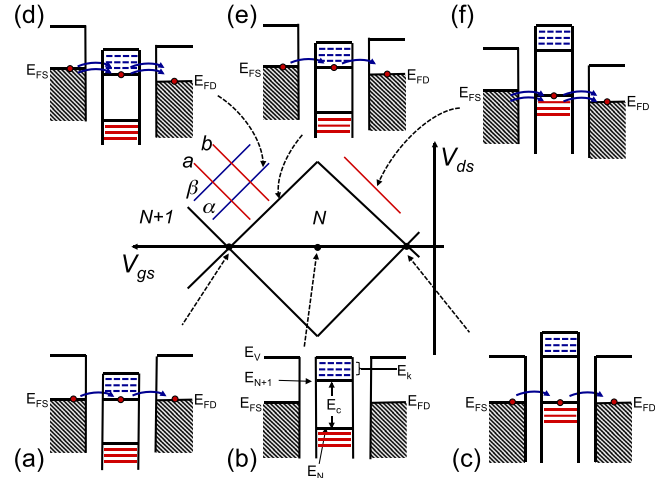


FIG. 4. Energy band diagrams for the single-hole transistor at specific points within a Coulomb diamond, with pathways for tunnelling shown by arrows. (a) $V_{ds} = 0$ V, $N+1$ valence band electrons on the island, and Coulomb blockade overcome. (b) Coulomb blockade fully imposed. (c) $V_{ds} = 0$ V, N valence band electrons on the island, and Coulomb blockade overcome. (d) Resonant tunnelling features parallel to the top left diamond edge. (e) Along the top left diamond edge. (f) Resonant tunnelling features parallel to the top right diamond edge.

(position (f)). In Fig. 2(c), for lines α – δ parallel to the top left edge, tunnelling occurs through excited states above the Coulomb gap and within the E_{FS} – E_{FD} window, with N electrons on the QD. For lines a – c parallel to the top right edge, tunnelling occurs through excited states below the Coulomb gap and within the E_{FS} – E_{FD} window, with $N+1$ electrons on the QD.

The energy spacing between adjacent lines is now extracted. By extending the lines to find the intersection points with the adjacent diamond edge, the V_{ds} value of this point gives the energy of the state relative to the Coulomb gap.³⁹ The energies for the first lines (a and α) are $E_a = 10$ meV and $E_\alpha = 15$ meV. The average energy spacing is $\Delta_1 = 11.2$ meV and $\Delta_2 = 15$ meV, in groups a – c and α – δ , respectively.

These energy scales are considered first within the excited state model. Here, a spherical QD of radius r may be assumed to represent the charging nanocrystal in our device. The energy of the first line E_a may then be associated with the first confinement state energy $E_1 = (\pi^2\hbar^2)/(2mr^2)$ in a spherical QD, giving $r \sim 7$ – 8 nm (using an effective mass for holes of 0.54). This is similar to the nanocrystal size and to the value extracted using E_c . On the other hand, in the phonon-assisted tunnelling model, $E_{p1} = \hbar\omega_p = 2\pi\hbar f_p = E_a$ and E_α , giving a phonon fundamental frequency $f_p = 2.4$ THz and 3.6 THz, respectively. Furthermore, some evidence exists for lines extending into the diamonds past their edges (Fig. 2(d)), a signature of phonon-assisted tunnelling.³⁸ Additional lines also cut into the diamonds, defining the arrowed features in Fig. 2(d).

A phonon assisted tunnelling model is also supported by comparing extracted energy scales to experimental and theoretical reports on the phonon spectrum of Si nanocrystals.^{11,26,27} Raman measurements have shown acoustic phonon peaks at 2.5 – 3.7 meV, in Si nanocrystals ~ 3 – 5 nm in diameter.¹¹ Theoretical calculations of the phonon density of states (DOS) in ~ 2 – 3 nm Si nanocrystals predict low energy acoustic-phonon peaks at 6 – 12 meV.^{26,27} For both Raman

measurements¹¹ and theoretical calculations,^{26,27} the first phonon peak energy is similar to our extracted value, $E_{p1} = 10\text{--}15$ meV, supporting a phonon-assisted tunnelling model.

In conclusion, a suspended *p*-type Si NW single-hole transistor, which includes <10 nm nanocrystal QDs, was fabricated using focused gallium ion beam exposure and anisotropic wet etching. Electrical characteristics at 10 K showed Coulomb diamonds having a charging energy ~ 27 meV, associated with a single dominant nanocrystal. Resonant tunnelling features with energy spacing ~ 10 meV were observed parallel to both diamond edges. While the patterns formed by these features may be associated with excited electron states, they may also be associated with hole-acoustic phonon interactions in the nanocrystal. In the latter case, the patterns are very similar to results observed in suspended CNT QDs, and the energy scale corresponds well to reported Raman spectroscopy results and phonon spectra calculations.

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